DO-178C Workflow with Qualified Code Generation

Simulation, Processor and Hardware in the Loop Test Cases Traceability to HLR

**SPECIFICATION**

**High Level Reqns**

- Model Conformance Checks
- Source Code Traceability to HLR

**Design Model**

**Low Level Reqns**

- Code Coverage
- Processor and Hardware in the Loop (P&H) Testing

**SOURCE CODE**

- Requirements Authoring
- Coverage Metrics
- Design Constraints

**EXECUTABLE OBJECT CODE**

- Embedded Coder
- Embedded Coder
- Embedded Coder

**DO Qualification Kit**

- Design Model
- Source Code
- Compiler
- Embedded Coder

**Effort Distribution in Model-Based Design Workflows**

- Specifications
- Test Design & Test Generation
- Unit Verification

**Effort Distribution in Traditional Development Workflows**

- Specifications
- Test Design & Test Generation
- Unit Verification